

Notice of References Cited	Application/Control No. 10/655,363	Applicant(s)/Patent Under Reexamination EDELLEN ET AL.	
	Examiner Alfred E. Dudding	Art Unit 2853	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,371,589 B1	04-2002	Conta et al.	347/14
	B	US-2001/0050410 A1	12-2001	Aswell, Cecil James	257/536
	C	US-2002/0060333 A1	05-2002	Tanaka et al.	257/299
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, Silicon Processing for the VLSI Era, 1990, Lattice Press, Volume 2, pp.354-356
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.